

**IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS: Ramesh Yarlagadda, Shwetal Desai, and Harish Devanagondi  
SERIAL NO.: 10/734,082  
FILING DATE: December 10, 2003  
TITLE: High Bandwidth Memory Management Using Multi-Bank DRAM  
Devices  
EXAMINER: Woo H. Choi  
GROUP ART UNIT: 2189  
ATTY. DKT. NO.: 23019-07460

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MAIL STOP AMENDMENT  
COMMISSIONER FOR PATENTS  
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**AMENDMENT B AND RESPONSE UNDER 37 CFR § 1.116**

Sir:

This amendment for the patent application identified above is in response to the Final Office Action dated July 21, 2006, which set a shortened statutory period for response that expires on October 21, 2006.

Amendments to the claims begin on page 2 of this paper.

Remarks begin on page 9 of this paper.

Kindly amend this application as indicated herein.